



2819

PATENT
Attorney Docket No.: AHA-02201

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Art Unit: 2819
Eric John Hewitt et al.) Examiner: JY
11/10/03
18
Serial No.: 09/826,443)
Filed: April 4, 2001)
For: **ENHANCED TURBO PRODUCT**) **TRANSMITTAL LETTER**
CODE DECODER SYSTEM) 162 N. Wolfe Road
) Sunnyvale, CA 90486
) (408) 530-9700
) Customer No. 28960

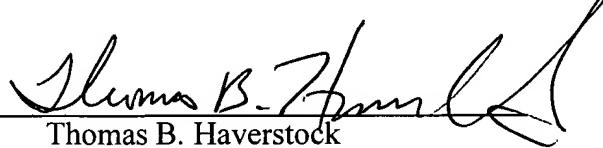
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find a Supplemental Information Disclosure Statement and Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

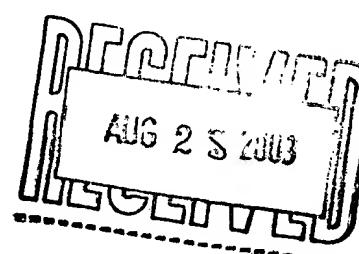
Respectfully submitted,
HAVERSTOCK & OWENS LLP

By: 
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

CERTIFICATE OF MAILING (37 CFR § 1.8(a))
I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the:
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HAVERSTOCK & OWENS LLP.
Date: 8-10-03 By: 





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Attorney Docket No.: AHA-02201

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Eric John Hewitt, *et al.*

Serial No.: 09/826,443

Filed: April 4, 2001

For: **ENHANCED TURBO PRODUCT
CODE DECODER SYSTEM**

) Group Art Unit: 2819

) Examiner:

) **SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT**

) 162 N. Wolfe Road
Sunnyvale, CA 94086
(408) 530-9700

) Customer No. 28960

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The citations listed below, a copy attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publications which may be material to the examination of this application:

- “AHA Galaxy TPC Windows Evaluation Software”, Advanced Hardware Architectures Preliminary Product Brief, Published after 4/4/00;
- “Turbo Product Code Encoding and Decoding with Quadrature Amplitude Modulation (QAM)”, Advanced Hardware Architectures Application Note, Published after 4/4/00, pages 1-11;
- “AHA4501 TPC EVM ISA Evaluation Module”, Advanced Hardware Architectures Preliminary Product Brief, Published after 4/4/00;
- “Turbo Product Codes for LMDS”, Advanced Hardware Architectures Preliminary Brief, Advanced Hardware Architectures Application Note, Published in 1999, pages 1-9;

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- “Using Multiple AHA4501 Devices in Parallel for Higher Data Rates”, Advanced Hardware Architectures Application Note, Published after 4/4/00, pages 1-5;
- “AHA4501 Astro”, Advanced Hardware Architectures Product Specification, Published after 4/4/00, pages 1-36;
- “Primer: Turbo Product Codes”, Advanced Hardware Architectures Application Note, Published after 4/4/00, pages 1-8;
- “Use and Performance of Shortened Codes with the AHA4501 Turbo Product Code Encoder/Decoder”, Advanced Hardware Architectures Application Note Published after 4/4/00, pages 1-5;
- “AHA4501 Astro 36 MBITS/SEC Turbo Product Code Encoder/Decoder”, Advanced Hardware Architectures Preliminary Product Brief, Published after 4/4/00, pages 1-5;
- “Galaxy Core Generator Turbo Product Code Decoder Cores”, Advanced Hardware Architectures Preliminary Product Brief, Published after 4/4/00, pages 1-3;
- “AHA Turbo Product Codes Frequently Asked Questions”, Advanced Hardware Architectures Application Note, Published after 4/4/00, pages 1-5;
- “AHA4501 Designer’s Guide”, Advanced Hardware Architectures Application Note, Published after 4/4/00, pages 1-9;
- “Use and Performance of the AHA4501 Turbo Product Code Encoder/Decoder with Differential Phase Shift Keying (DPSK), Advanced Hardware Architectures Application Note, Published after 4/4/00, pages 1-4;
- “Galaxy TPC Cores Turbo Product Code Encoder/Decoder Cores”, Advanced Hardware Architectures Preliminary Product Brief, Published after 4/4/00, pages 1-3; and
- “AHA4501 TPC Windows Evaluation Software”, Advanced Hardware Architecture Preliminary Product Brief, Published after 4/4/00.

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This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated: 8-1-03

By: Thomas B. Haverstock
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

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HAVERSTOCK & OWENS LLP.
Date: 8-1-03 By: J. M. H.